

Shift	Input					Output		
					A	B	C	D
(0)	0	1	0	1	0	0	0	0
(1)	x	0	1	0	1	0	0	0
(2)	x	0	1	0	1	0	0	0
(3)	x	x	x	0	1	0	1	0
(4)	x	x	x	x	0	1	0	1

FIG.1A
PRIOR ART

Shift	Input				Output			
	B	D	A	C				
(0)	1	1	0	0	0	0	0	0
(1)	x	1	1	0	0	0	0	0
(2)	x	x	1	1	0	0	0	0
(3)	x	x	x	1	1	0	0	0
(4)	x	x	x	x	1	1	0	0

FIG.1B
PRIOR ART

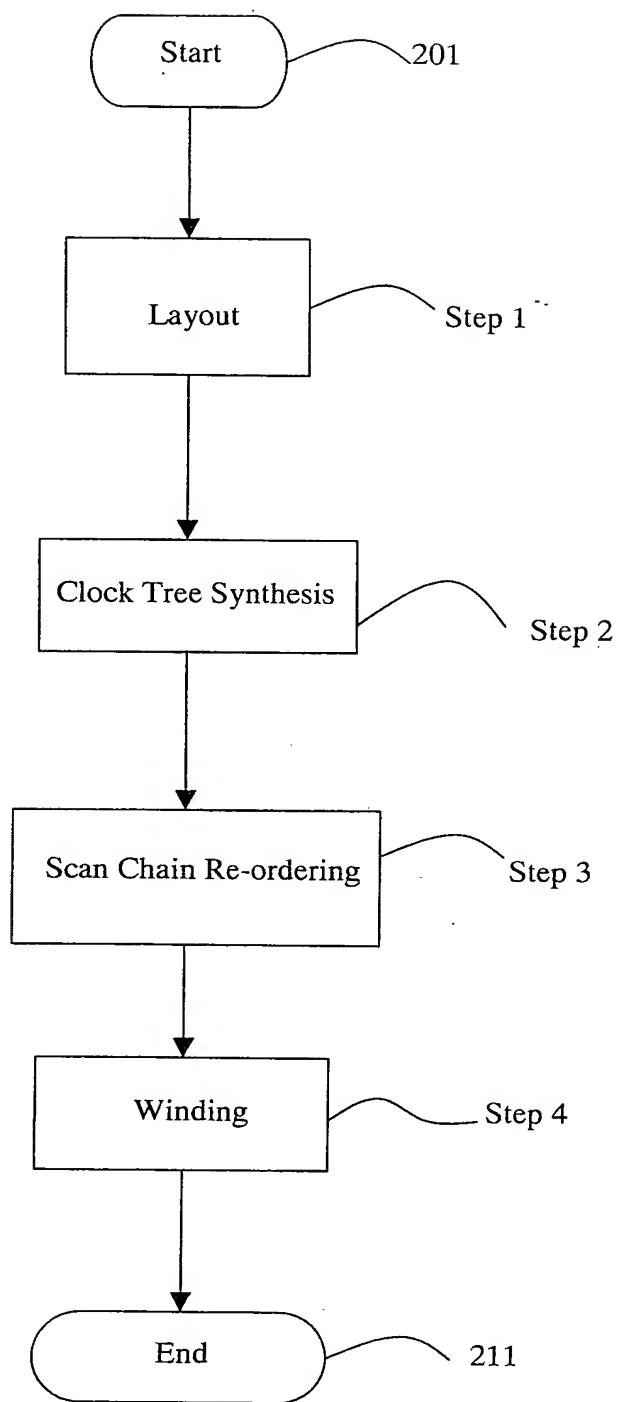


FIG.2
PRIOR ART

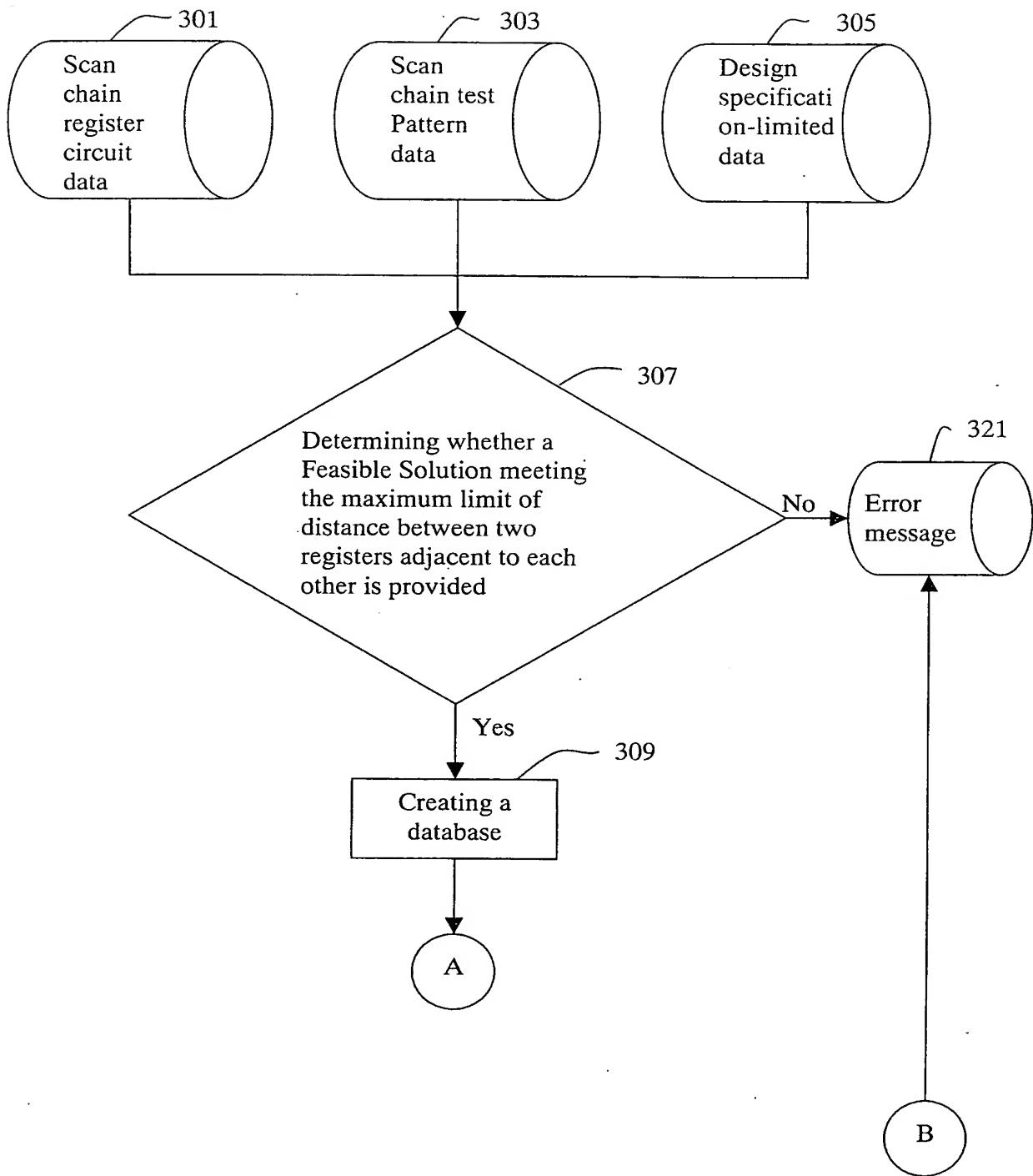


FIG.3A

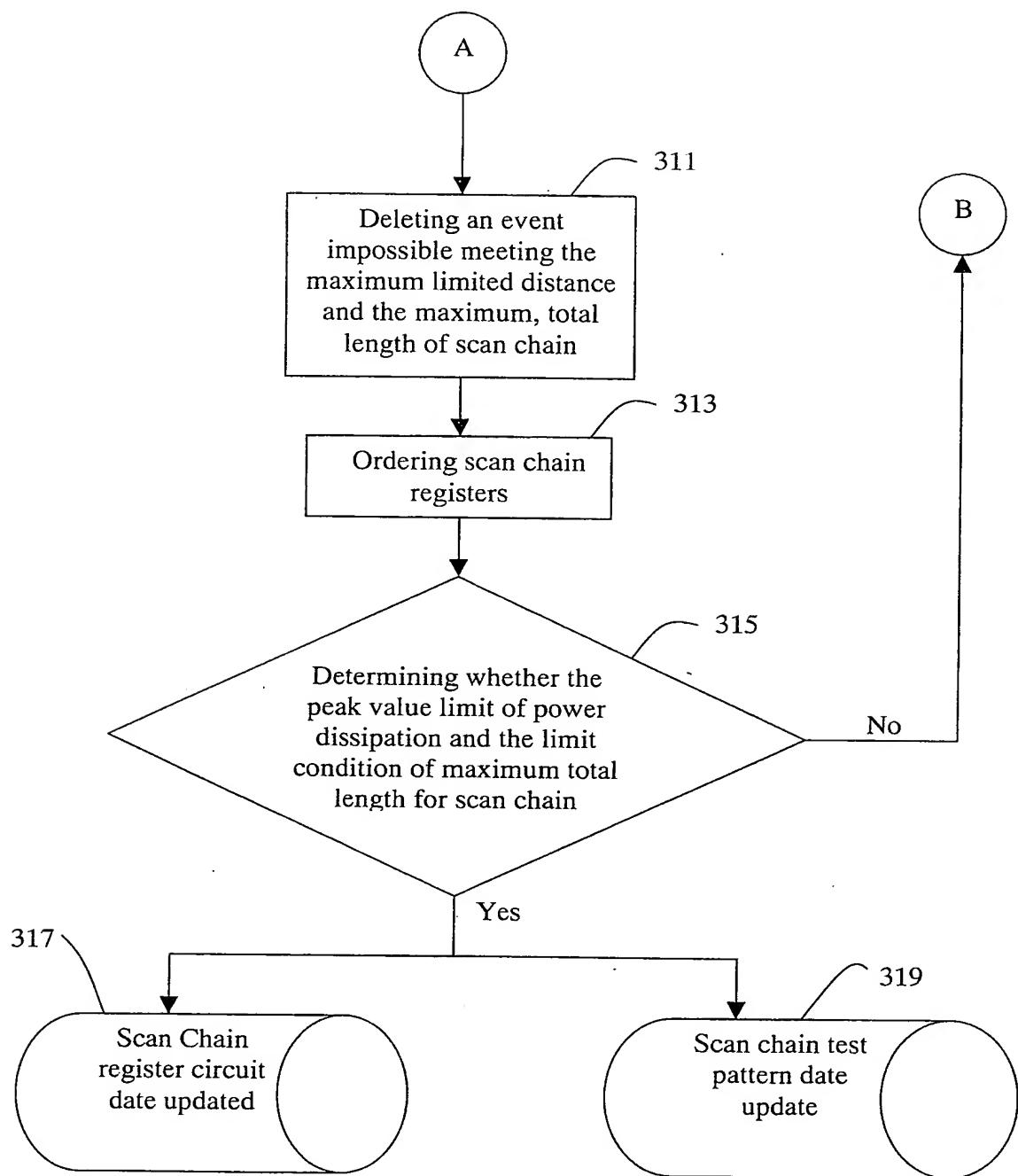


FIG.3B

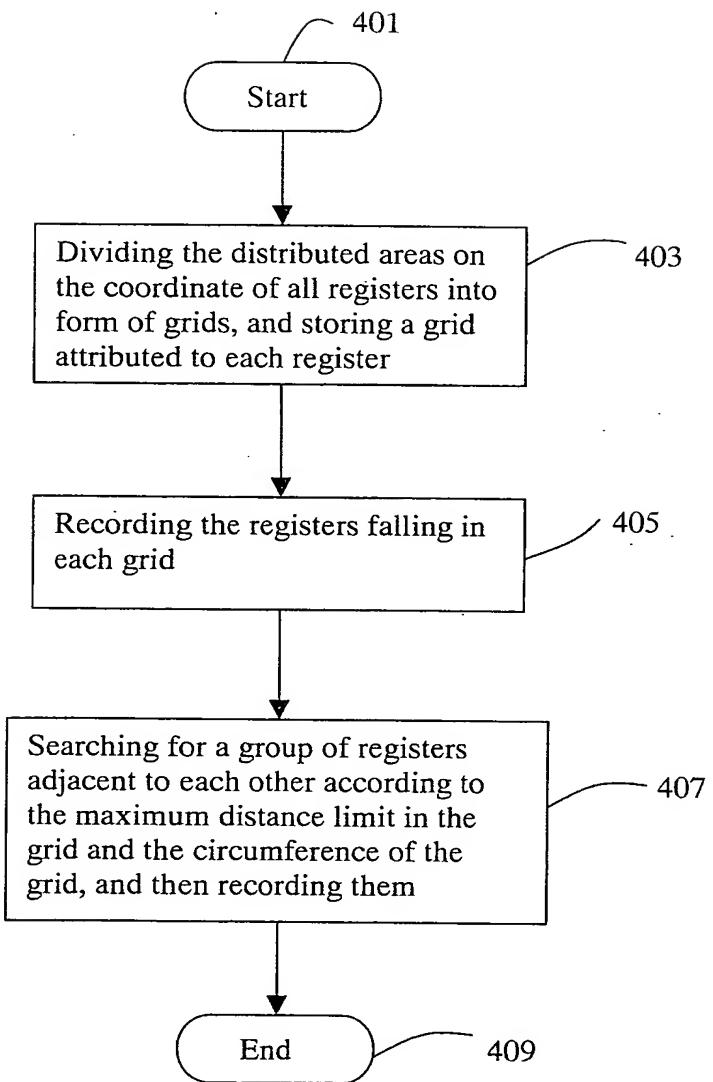


FIG.4